

# Curriculum Vitae

	<b>Anandaraj SP</b> anandsofttech@gmail.com	<b>Address for Communication</b> 2-296, sathya sayee colony, VidyaRanya Puri, Bheemaram, Warangal Dt.
	<b>DATE OF BIRTH:</b> 16-01-1982	Phone: 09966846347,09494873447

## Objective:

Seeking a position to utilize my skills and abilities in your institution offers security and professional growth while being resourceful, innovative and flexible.

## Academic Profile:

Qualification	Institution /School	Year of Passing	Percentage Of marks
Ph.D (Computer Science)	St.Peters University	Jan 2013	-
M.Tech ( CSE)-HONOUR	Dr. M.G.R University	May 2007	93.97
B.E ( CSE) - First Class	V.R.S College Of Engineering, Villupuram	April, 2004	74.35
H.S.C - First Class	Govt.Hr.Sec. School, pennagar.	April, 2000	68.75
SSLC - Distinction	Govt.Hr.Sec. School, pennagar.	April, 1998	82

## Salient Projects:

Project Name	<b>TCP/IP based on AIMD using Multicast Congestion Control</b>		
Stream	M.TECH Academic Project		
Project Description	To control the traffic in the multicast Network		
Tools Used	Red Hat Linux & Windows		
Duration	Dec 2006 – April 2007	Team Size	1

Project Name	<b>DESIGN AND IMPLEMENTATION OF PC BASED PCB DRILLING MACHINE</b>		
Stream	B.E Academic Project		
Project Description	To automate the process of pcb drilling		
Tools Used	VC++		
Duration	Oct 2004 – Nov 2004	Team Size	1

Project Name	<b>DESIGN OF COUNTERS</b>		
Stream	B.E Academic mini Project		
Project Description	Counting the objects		
Tools Used	C		
Duration	Jan 2005 – March 2005	Team Size	1

## Technical Knowledge:

Software	
Operating Systems	MS DOS, Windows 9x, NT, 2000, XP,LINUX
Programming Languages	C, C++,Visual Basic 6.0, C , Java(Core), UML 1.0
Databases	MS Access, Oracle
Testing tool	Win runner, Load runner

## Working Experience:

- 1 Worked as a Lecturer in **Priyadarshini Engineering College**, Vaniyambadi, and Vellore. From June 2004 to may 2006.
- 2 Lecturer in Sri **Venkateswara College of Engineering and Technology**, Chittoor, from June 2006 to Nov 2007.
- 3 Worked as **Asst. Professor** in **SR Engineering College**, Warangal from Dec 2007 to Dec-2010.
- 4 Working as **Sr. Asst. Professor** in **SR Engineering College**, Warangal from Jan 2011 to till date.

## Technical Activities:

### Journals:

- [1] **S.P. Anandaraj**, S.Poornima, "Structuring UML with Unified Approach using Database modeling", *International Journal of high performance and communication systems*, ISSN 0975 3974.
- [2] **S.P. Anandaraj**, Dr. S. Ravi, S.Poornima, "Fault Tolerant implementation of Xilinx Vertex FPGA through On-Chip System Evolution", *SPRINGER-VERLAG*, Library of congress control Number: 2010940170, ISSN 1865-0929.
- [3] K. Ravi Chythanya, **S.P.Anandaraj**, S. Padmaja, " Virtual Energy-Efficient Keying for wireless sensor networks", *International Journal on Computer Science and Engineering(IJCSE)*, ISSN:2229-5631, Volume:3,Issue:8,EISSN:0975-3397,Aug2011,ppno:1663 to 1671.

- [4] **S.P.Anandaraj**, V. Anil Kumar, "Enhanced Security through Dynamic Broadcast Routing" *International Journal of Advanced Research in Computer Science(IJARCS)*, ISSN 0976-5697, Volume:2, Issue:5, Sept-Oct 2011 ,ppno: 148 to152.
- [5] **S.P. Anandaraj**, Dr. S. Ravi, S.Poornima, "Design of Wireless Sensor Architecture using XC2V40 Vertex-II with Xilinx Embedded FPGA Integrated Software Environment", *International journal of computer science and software technology(IJCSST)*, ISSN:0974-3898, Volume:5, Issue:1;jan-june 2012, ppno: 9-12.

## Conferences:

1. S.P.Anandaraj, Dr. S. Ravi, S.Poornima, "**Implementation of EHW for fault tolerant Computation on Multiple Sensor Application including Interrupt Driven Routines enabling Wireless through IEEE 802.15.4 protocol suite**" *International Conference AICERA-2011*, ISBN: 978-81-9213320-0-6.
2. S.P.Anandaraj, Dr. S. Ravi, S.Poornima, "**Fault Tolerant Implementation of Xilinx Vertex FPGA through On-Chip System Evolution**", *International Conference, FGCN-2010*.
3. S.P.Anandaraj, Dr. S. Ravi, S.Poornima, "**Realization of on-chip flexibility using Xilinx Vertex FPGA through target EHW**" *IEEE ICET'2010*.
4. S.P.Anandaraj, Dr. S. Ravi, S.Poornima, "**Integration of fault tolerant mechanisms in wireless sensor Networks through adopting SARD Architecture via Software Driven Approach**" *CCSN-2010*.
5. S.P.Anandaraj, Dr. S. Ravi, S.Poornima, "**Progression of EHW Chips in Streaming Applications**", *4<sup>th</sup> International Conference on "Managing in New world order: Strategies for Sustainable Business Development-2010"*.
6. S.P.Anandaraj, S.Poornima, "**Fuzzy Knowledge model in Concurrent Design**" *International Conference on "TRANSCENDING HORIZONS TROUGH INNOVATIVE GLOBAL PRACTICES - 2009"*.
7. S.P.Anandaraj, S.Poornima, "**Structuring UML with Unified Approach using Database modeling**", *NCAC09*, Paper id NCAC225, ISBN number 978-81-909376-0-3.
8. S.P.Anandaraj, S.Poornima, "**Virtualization for the Management Security of Business resources in IT Industry**", *International conference in "SUSTAINING BUSINESS IN A CHANGING GLOBAL ENVIRONMENT- 2009"*.

## Workshops/Seminars:

- 1 Attended **Entrepreneurship Development Program** in 2004 in VRS college of Engineering and Technology, Villupuram.
- 2 Attended National Seminar on "**Web Information science and Technology**" in SCSVMV University, Kanchipuram, Vellore on 30<sup>th</sup> Sep 2008 & 1<sup>st</sup> Oct 2008.
- 3 Attended two day workshop on "**Middleware Technologies**" on 12<sup>th</sup> & 13<sup>th</sup> Dec 2008 in SREC, Warangal.
- 4 Attended Two-day workshop on "**Application of Computer Graphics and Image Processing Techniques in Medical Imaging**" on 26<sup>th</sup> & 27<sup>th</sup> Feb 2010 in VIT, Vellore.

- 5 Attended one day workshop on” **An overview of research & innovation**” In SREC organized by ISTE, New Delhi on 1<sup>st</sup> may 2010.
- 6 Attended one-Day Tutorial on” **Web Services: Architectures, Design and Deployment**” on 5<sup>th</sup> march 2011 in SREC, Warangal.
- 7 Attended one-Day Seminar on ”**Patent Based Projects**” In SREC organized by ISTE, New Delhi on 12<sup>th</sup> May,2011.
- 8 Attended Two Day Workshop On ”**Windows & Linux Trouble shooting**” at SVS Group of Institutions, Warangal Held on 30<sup>th</sup> & 31<sup>st</sup> July 2011.
- 9 Attended Two day workshop IUCEE on” **Globally relevant electrical and computer technology curriculum**” held on 12<sup>th</sup> & 13<sup>th</sup> Aug 2011 at KGRCE, Hyderabad.

### Faculty Enrichment Programs:

1. Attended **FDP** on “**XML and Web Services**” from 10-11<sup>th</sup> August 2009 in SVCE, Chennai organized by **IBM**.
2. Attended **Faculty Empowerment Program** on “**MISSION 10X**” from 17<sup>th</sup>-22<sup>nd</sup> Jan 2011 in SREC, Warangal, Organized by **WIPRO**.
3. Attended **FDP** on “**Ripple-Creators**” from 21-22<sup>nd</sup> Oct 2011 in CTS campus, Hyderabad organized by **CTS**.

### Programs Organized:

1. Organizing Member in two-day workshop on “**Middleware Technologies**” on 12<sup>th</sup>& 13<sup>th</sup> Dec 2008 in SREC, Warangal.
2. Committee Member of National Level Technical Symposium “**Technotrendz-09**” on 13<sup>th</sup> March 09 in SREC, Warangal.
3. Active member in Organizing National level Technical Symposium “**Amogha-11**” on 12<sup>th</sup> March in SREC, Warangal.

### Achievements:

- [1] **Additional Controller Examinations** for SREC Autonomous from **Jan 2012 to till Date**.
- [2] **Co-Ordinator** for **JNTU Nodal Centre Spot Valuation** from **Dec 2008 to Dec 2011** in SREC, Warangal.
- [3] **Co-Ordinator** for ISTE, CSI Membership in cse dept at SREC, Warangal from **Dec 2010 to Dec 2012**.
- [4] Been as a Seminar **Co-Ordinator** for Final Year Engineering Students in Priyadarshini Engineering College, Vaniyambadi, and Vellore from **May 2004 to June 2006**.

- [5] Been as a Project **Co-Ordinator** for Final Year CSE Students in Sri Venkateswara College of Engineering and Technology. Chittoor.
- [6] Been as an Assistant for Placement Co-Ordinator in Sri Venkateswara College of Engineering and Technology, Chittoor.
- [7] Been as a project Co-Ordinator for Final Year general Seminar & B.Tech, M.Tech Final Year Projects in SREC, Warangal.
- [8] Member of **TEQUIP** Phase-II Task Force team in SREC, Warangal.
- [9] Member of **NBA accreditation Team** at SREC, Warangal.
- [10] Member of "**Board of Studies**" Committee in SREC Autonomous For Academic Council in SREC, Warangal.

### Extracurricular activities:

- Organized a **INDUSTRIAL VISIT** with final year IT students to Majestic People InfoTech Pvt.Ltd, Coimbatore at **10- sep-2010**.

### Projects Handled:

#### **B.Tech (U.G):**

1. Guided more than 18+ projects For B.tech CSE students in various fields from may 2005.
2. Guided IBM TGMC Projects for 2 batches in academic year 2009-2010.
3. Guided IBM TGMC Projects for 2 batches in academic year 2010-2011.

#### **M.Tech (P.G)**

1. Guiding 3 batches for M.tech CSE students in academic year 2011-2012.
2. Guided 3 batches for M.tech CSE students in academic year 2010-2011.

### Papers Handled:

#### **B.Tech (U.G):**

More than twenty subjects dealed for b.tech

#### **M.Tech (P.G)**

1. Java & Web Technologies
2. Information Security.
3. Embedded Systems.
4. Distributed Computing.

### Seminar Papers Handled:

1. Image Recognition.
2. Text Recognition.
3. Online Processing.
4. Wireless Application Protocol (WAP).
5. Wireless Sensor Networks

## Grants:

- Reward of Money Rs.5000\ - for achieving more than 50% of distinctions for IV IT in OCT' 2010.

## Personal Details:

Name : **S.P. ANANDARAJ**  
Father's Name : G.PICHANDI  
Age : 28 Years  
Date of birth : 16th JANUARY 1982  
Nationality : Indian  
Sex : Male  
Marital Status : Married  
Languages Known : English, Telugu, and Tamil  
Permanent Address : 2-293, vidhranyapuri, sathya sayee colony,  
Bheemaram Warangal – 506 009  
Home : 09494873447  
Personal : 09966846347  
Email : anandsofttech@gmail.com

## Declaration:

I hereby declare that all the information furnished is true to the best of my knowledge.

**Station:**

Yours faithfully,

**Date :**

**(ANANDARAJ S.P)**